

Addendum to MC68HC908AP64 Data Sheet, Rev. 2.5

This addendum provides update to the *MC68HC908AP64 Data Sheet*, Rev. 2.5
(Freescale document number MC68HC908AP64/D).

Page 417:

Update *Output high voltage* and *Output low voltage* parameters in
Table 24-4 DC Electrical Characteristics (5V).

From:

Characteristic	Symbol	Min	Typ	Max	Unit
Output high voltage ($I_{LOAD} = -12\text{mA}$) PTA[0:7], PTB[4:7], PTC[0:5], PTD[0:7]	V_{OH}	$V_{DD}-0.8$	—	—	V
Output low voltage ($I_{LOAD} = 8\text{mA}$) PTA[0:7], PTB[4:7], PTC[0:5], PTD[0:7]	V_{OL}	—	—	0.4	V
($I_{LOAD} = 15\text{mA}$) PTB[0:3], PTC[6:7]	V_{OL}	—	—	0.4	V
($I_{LOAD} = 15\text{mA}$) as TxD, RxD, SCTxD, SCRxD	V_{OLSCI}	—	—	0.4	V
($I_{LOAD} = \text{see Table 24-12}$) as SDA, SCL	V_{OLIIIC}	—	—	0.4	V

To:

Characteristic	Symbol	Min	Typ	Max	Unit
Output high voltage ($I_{LOAD} = -12\text{mA}$) PTA[0:7], PTB[4:7], PTC[0:5], PTD[0:7]	V_{OH}	$V_{DD}-0.8$	—	—	V
($I_{LOAD} = -15\text{mA}$) PTA[0:7], PTB[4:7], PTC[0:5], PTD[0:7]	V_{OH}	$V_{DD}-1.0$	—	—	V
Output low voltage ($I_{LOAD} = 6\text{mA}$) PTA[0:7], PTB[4:7], PTC[0:5], PTD[0:7]	V_{OL}	—	—	0.4	V
($I_{LOAD} = 12\text{mA}$) PTB[0:3], PTC[6:7]	V_{OL}	—	—	0.4	V
($I_{LOAD} = 15\text{mA}$) PTA[0:7], PTB[4:7], PTC[0:5], PTD[0:7]	V_{OL}	—	—	0.8	V
($I_{LOAD} = 15\text{mA}$) PTB[0:3], PTC[6:7]	V_{OL}	—	—	0.6	V
($I_{LOAD} = 15\text{mA}$) as TxD, RxD, SCTxD, SCRxD	V_{OLSCI}	—	—	0.4	V
($I_{LOAD} = \text{see Table 24-12}$) as SDA, SCL	V_{OLIIIC}	—	—	0.4	V

Pages 419 and 423:

Correct *Internal oscillator clock frequency* parameters in
Table 24-6 Oscillator Specifications (5V) and **Table 24-10 Oscillator Specifications (3V)**.

From:

Characteristic	Symbol	Min	Typ	Max	Unit
Internal oscillator clock frequency	f_{ICLK}	16k	22k	26k	Hz

To:

Characteristic	Symbol	Min	Typ	Max	Unit
Internal oscillator clock frequency	f_{ICLK}	64k	88k	104k	Hz

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